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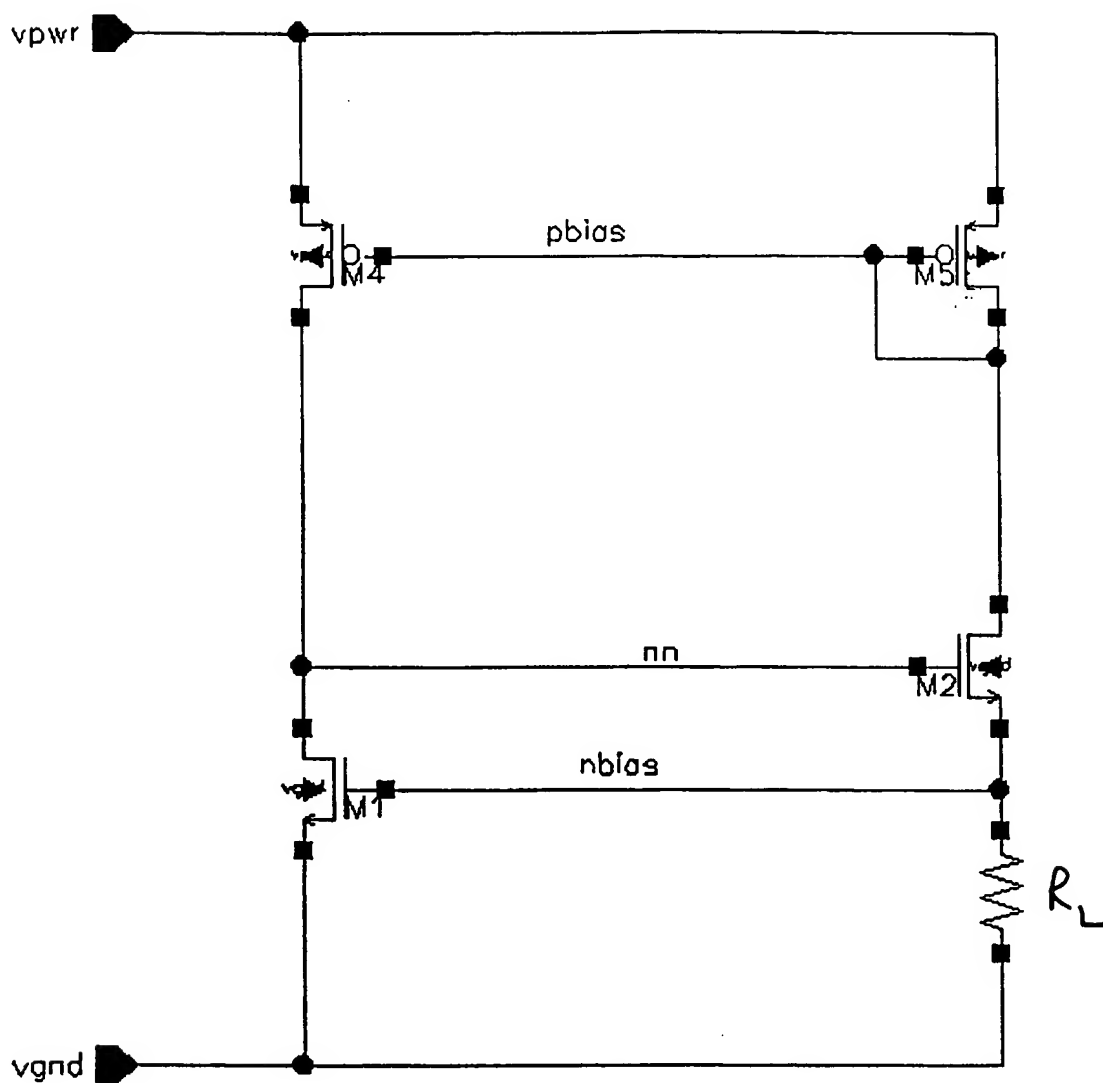


FIG. 1

CIRCUIT AND METHOD FOR IMPLEMENTING A LOW SUPPLY
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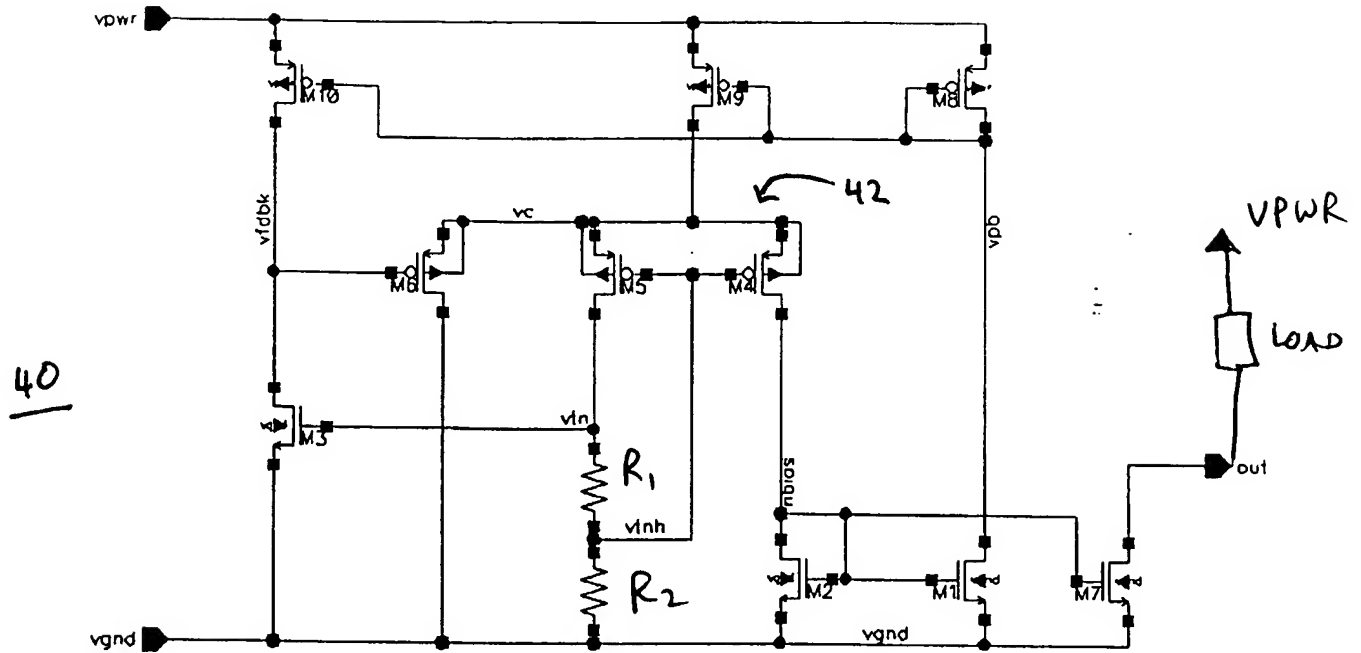


FIG. 2

CIRCUIT AND METHOD FOR IMPLEMENTING A LOW SUPPLY
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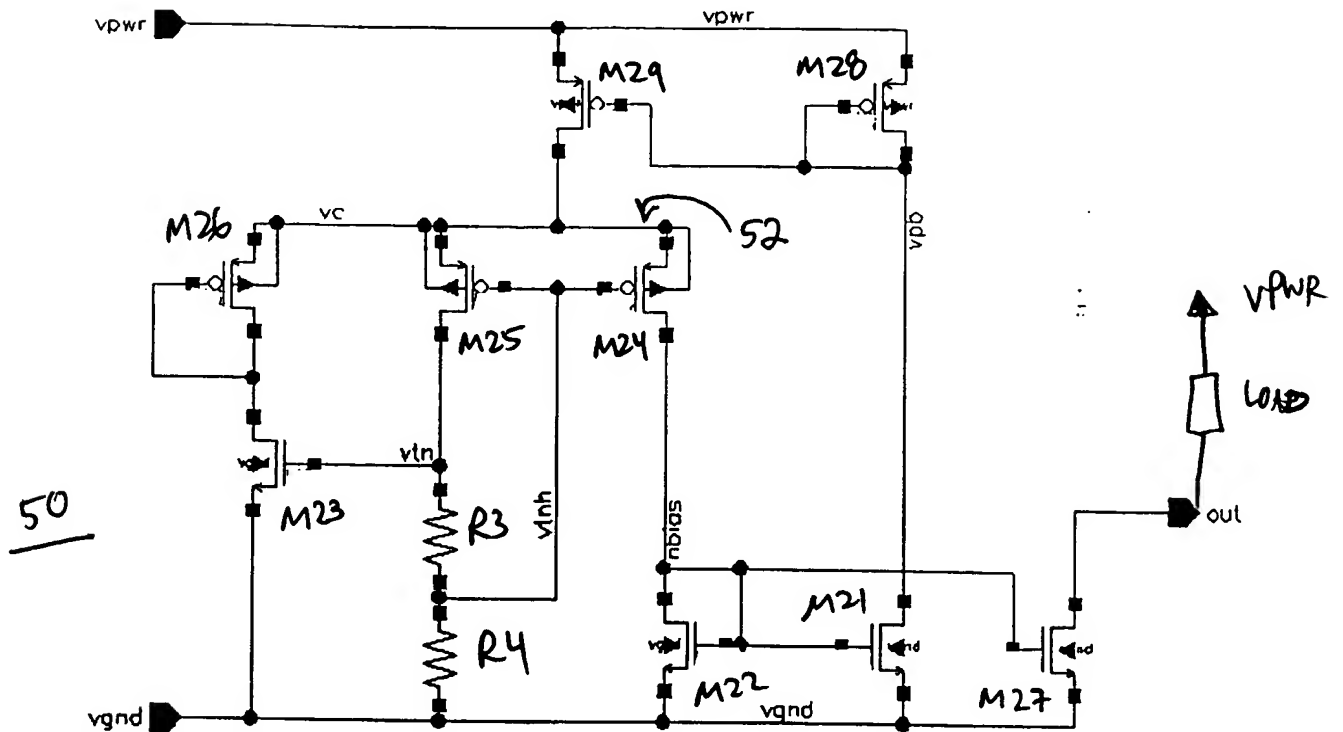


FIG. 3

CIRCUIT AND METHOD FOR IMPLEMENTING A LOW SUPPLY
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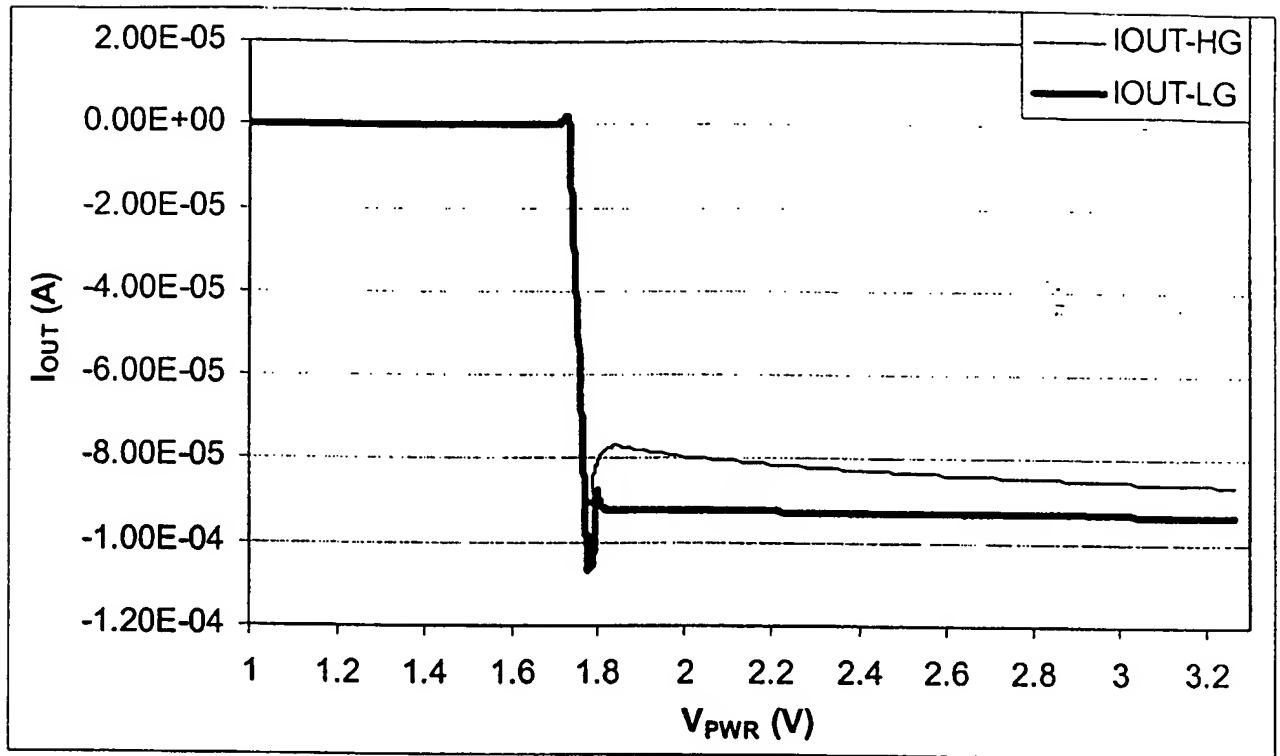


FIG. 4